

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): **Koga, Hiroki**

Serial No.: N/A (Parent Application
09/991,093)

Filed: **Herewith**

Title (as Amended): **Semiconductor Device
Including an Insulated Gate Field
Effect Transistor and Method for
Manufacturing the Same**

Attorney Docket No.: **N34771600WD1**

Group Art Unit (of Parent Application): **2814**

Examiner: **N/A**

PRELIMINARY AMENDMENT

5 Mail Stop Patent Applications
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

10

A. Introductory Comments

The following is a Preliminary Amendment for the attached patent application.

15

20

37 C.F.R. §1.10

25 I hereby certify that this is being deposited with the U.S. Postal Service "Express Mail Post Office to Addressee" service under
37 CFR § 1.10 on the date indicated below and is addressed to:

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

30

By:  _____

35

Typed Name: _____ Darryl G. Walker _____

Express Mail Label No.: E044052550105

40

Date of Deposit: September 18, 2003